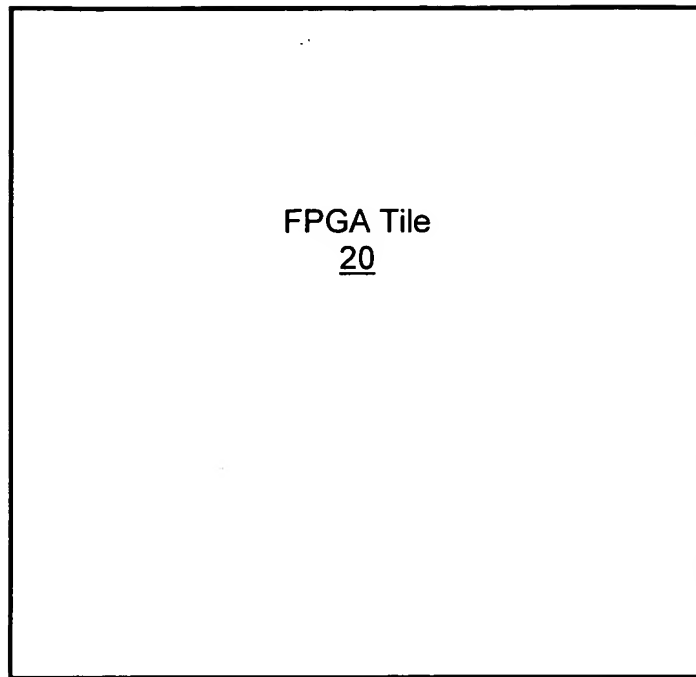
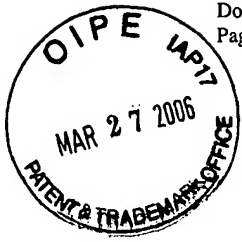


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Attorney: William P. Wilbar
Sierra Patent Group, Ltd.

Replacement Drawing



FPGA Tile
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FIG. 1.

Replacement Drawing

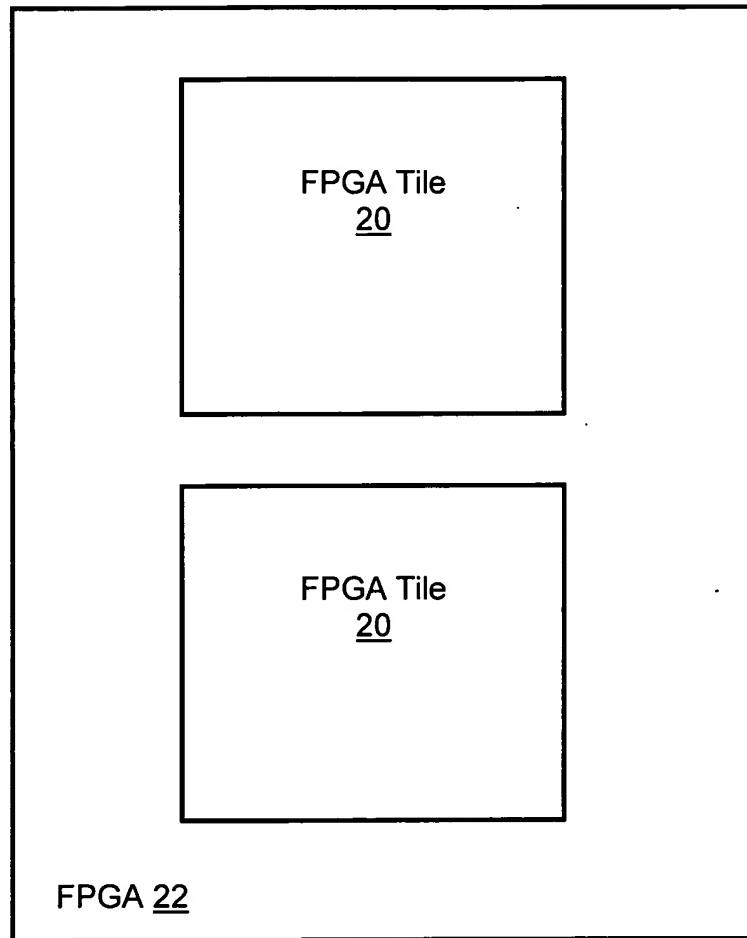


FIG. 2

Replacement Drawing

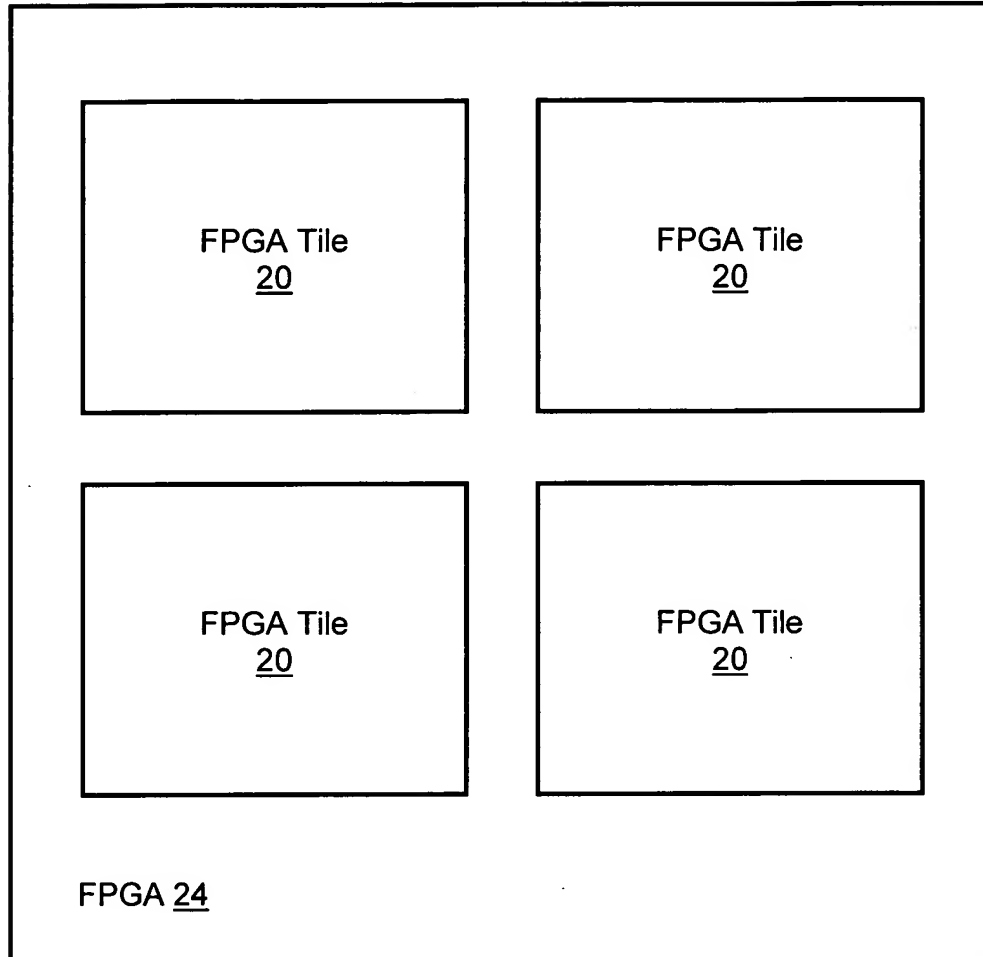


FIG. 3A

Replacement Drawing

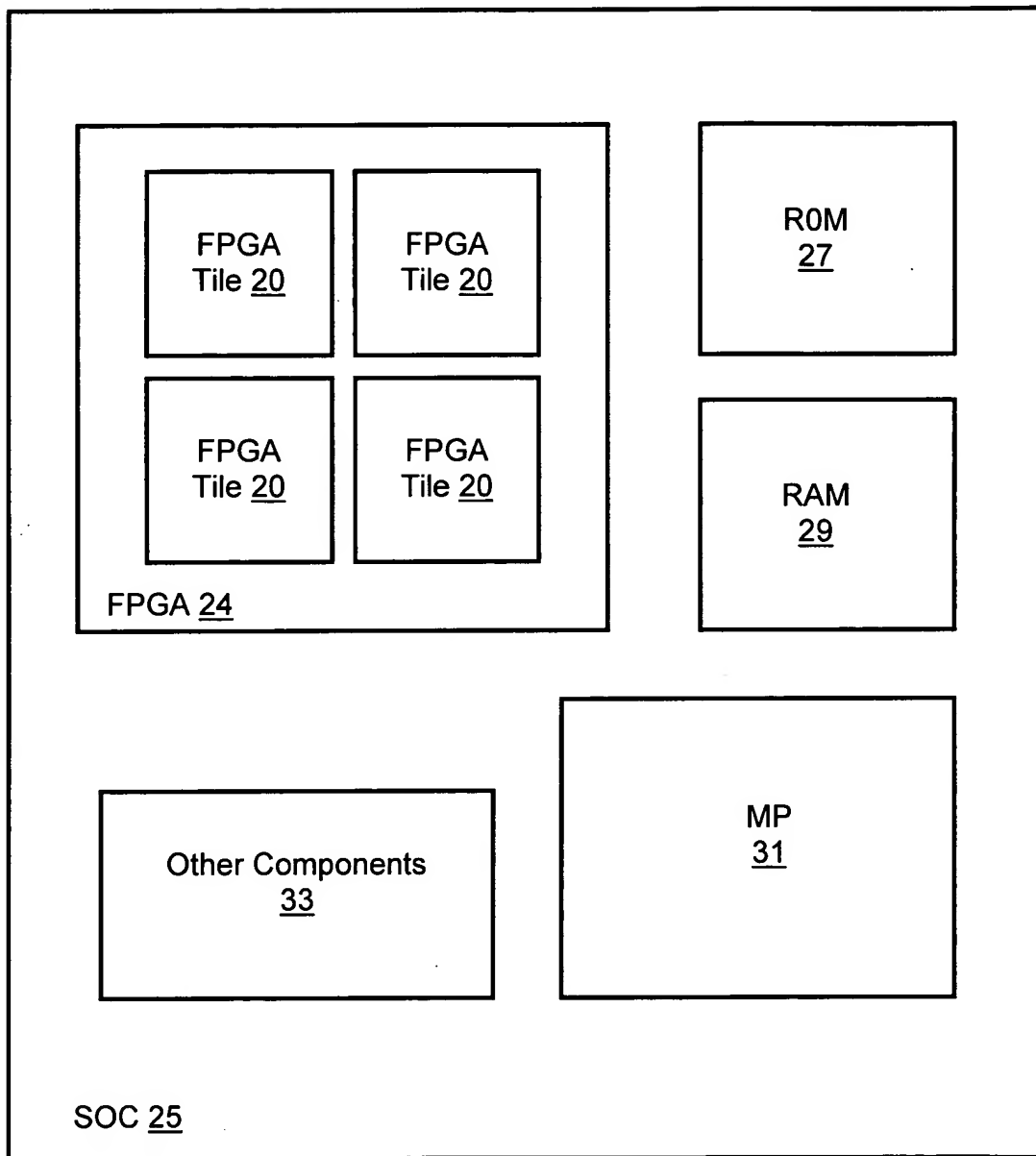


FIG. 3B

Replacement Drawing

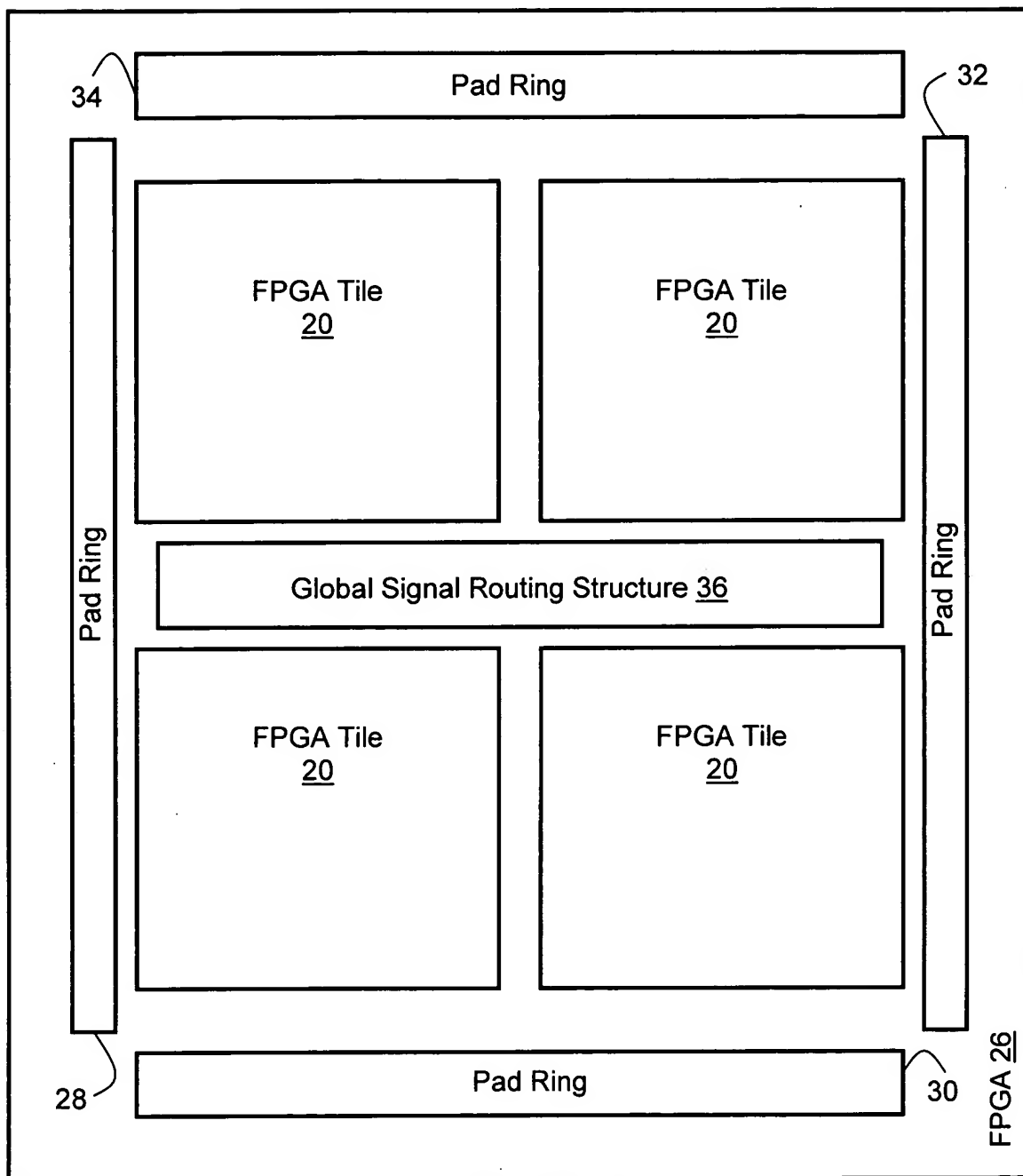


FIG. 4

Replacement Drawing

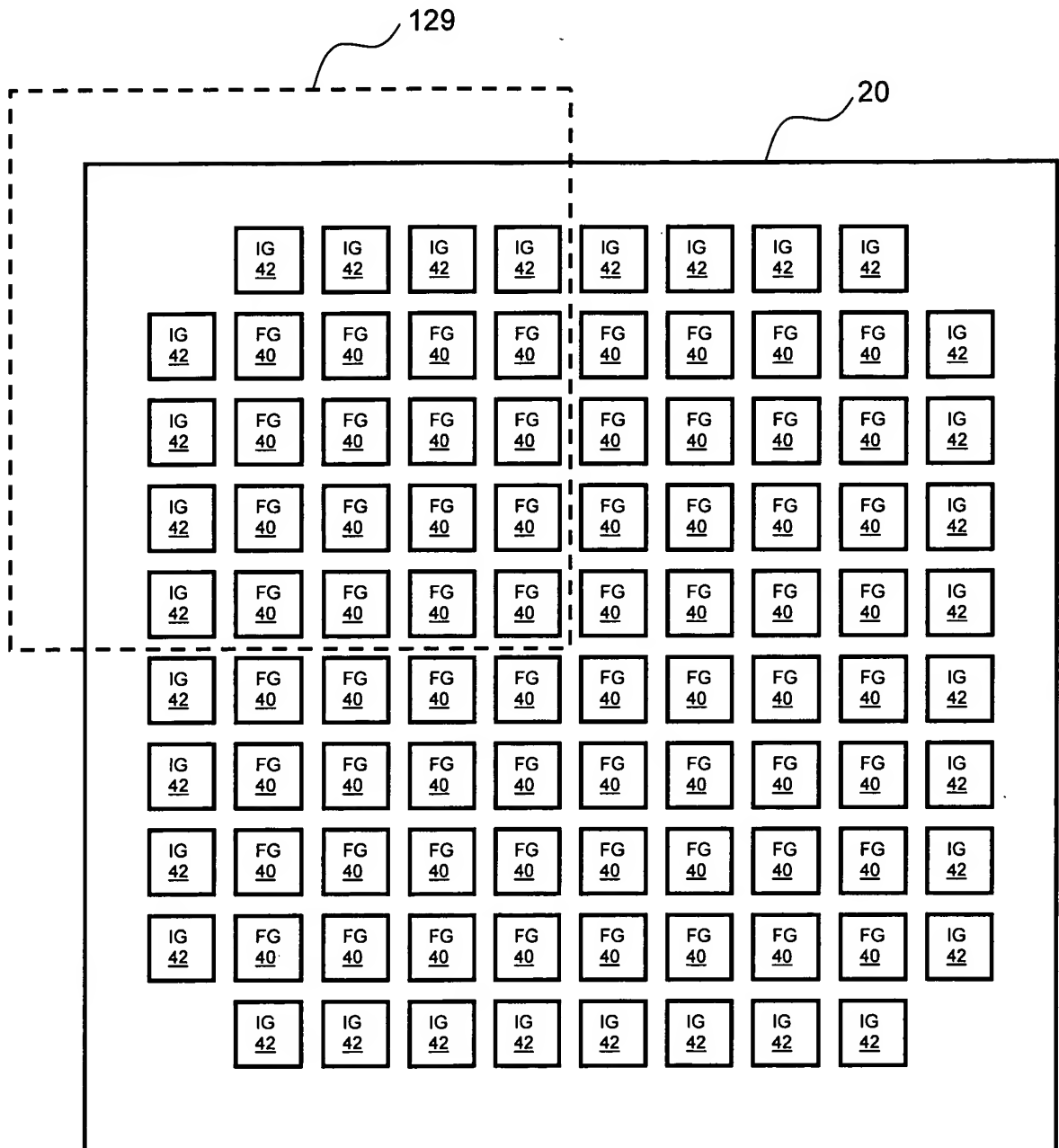


FIG. 5

Replacement Drawing

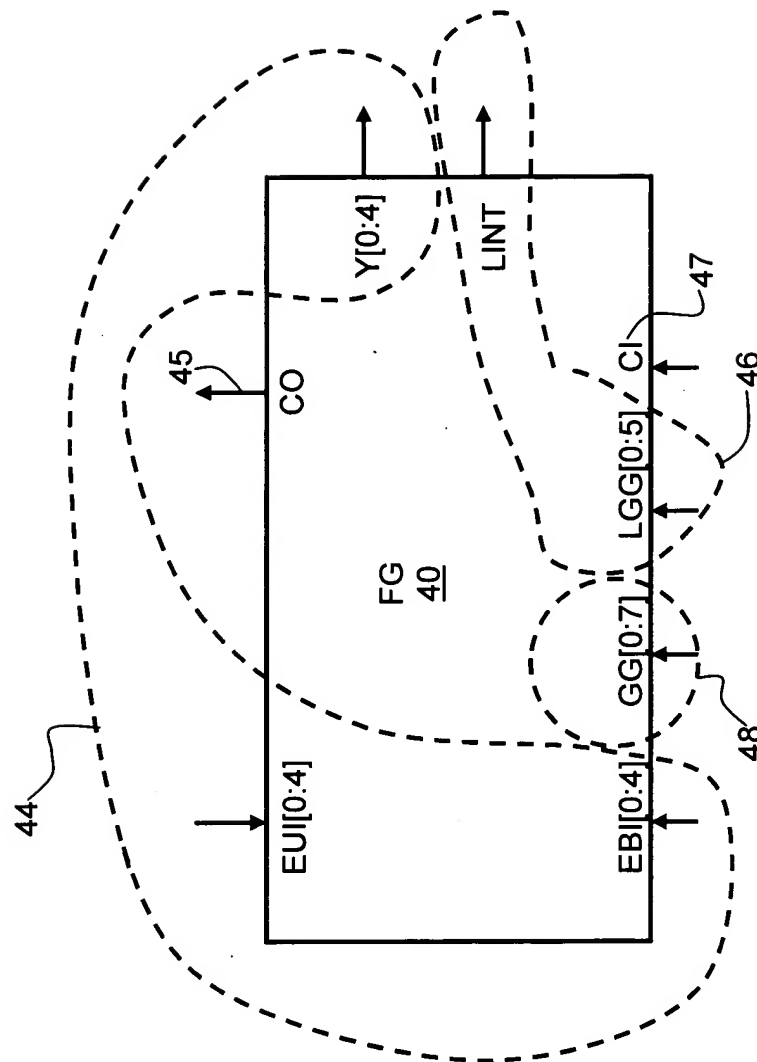
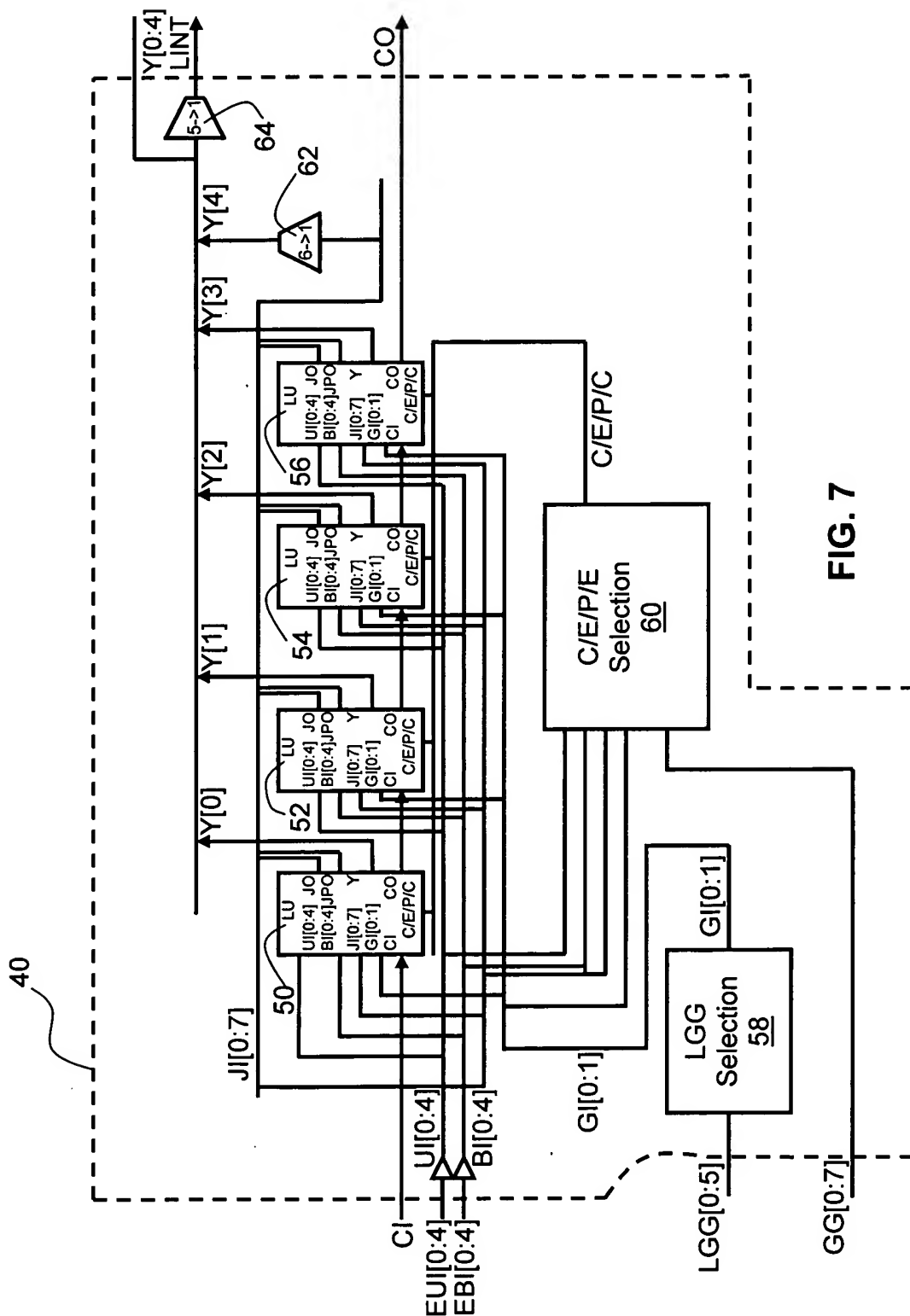


FIG. 6

Replacement Drawing



Replacement Drawing

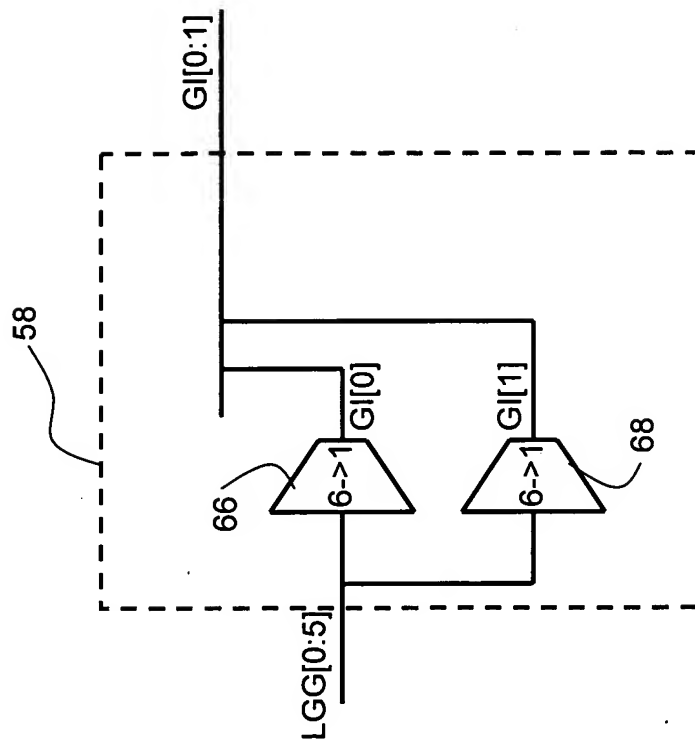


FIG. 8

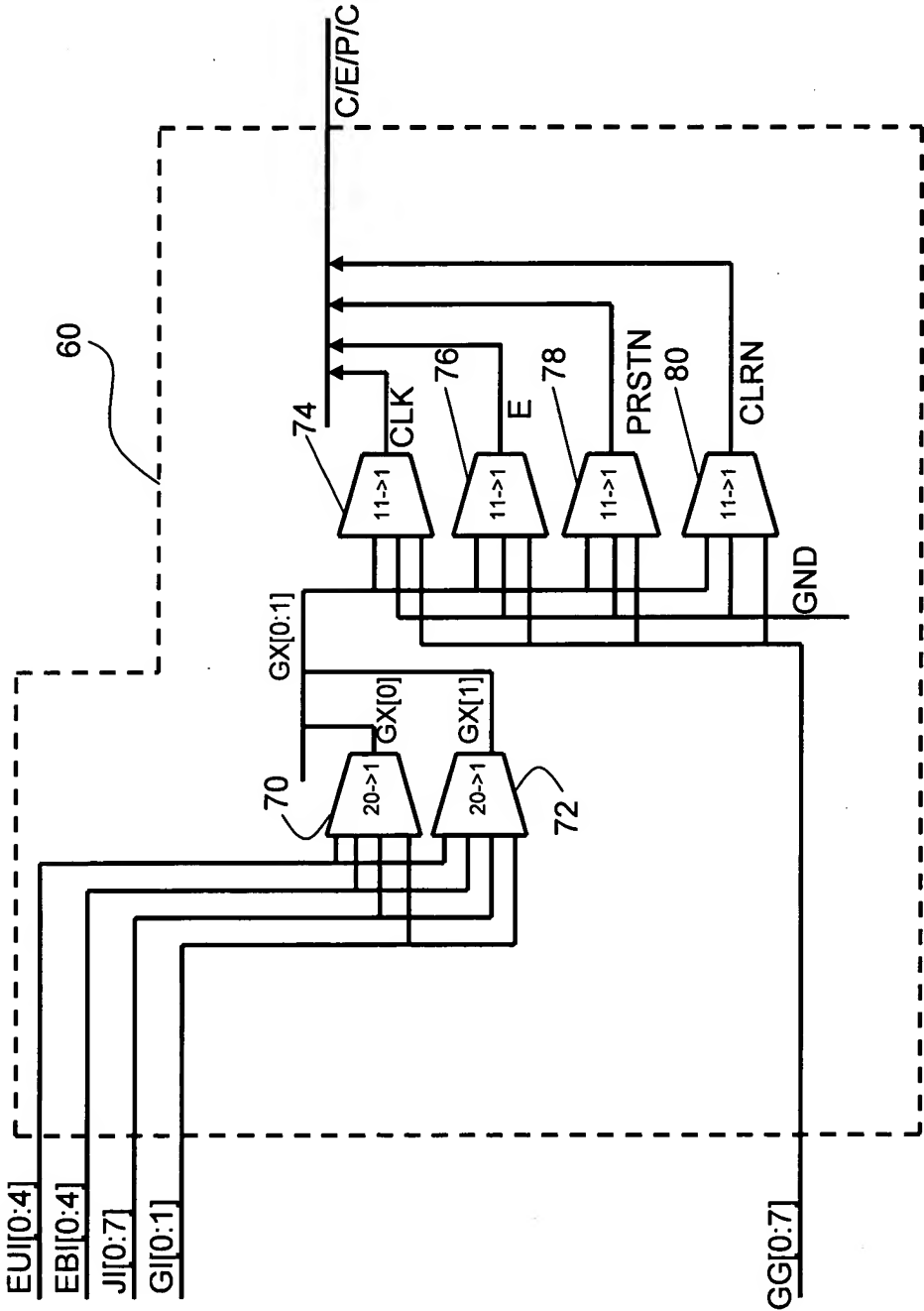


FIG. 9

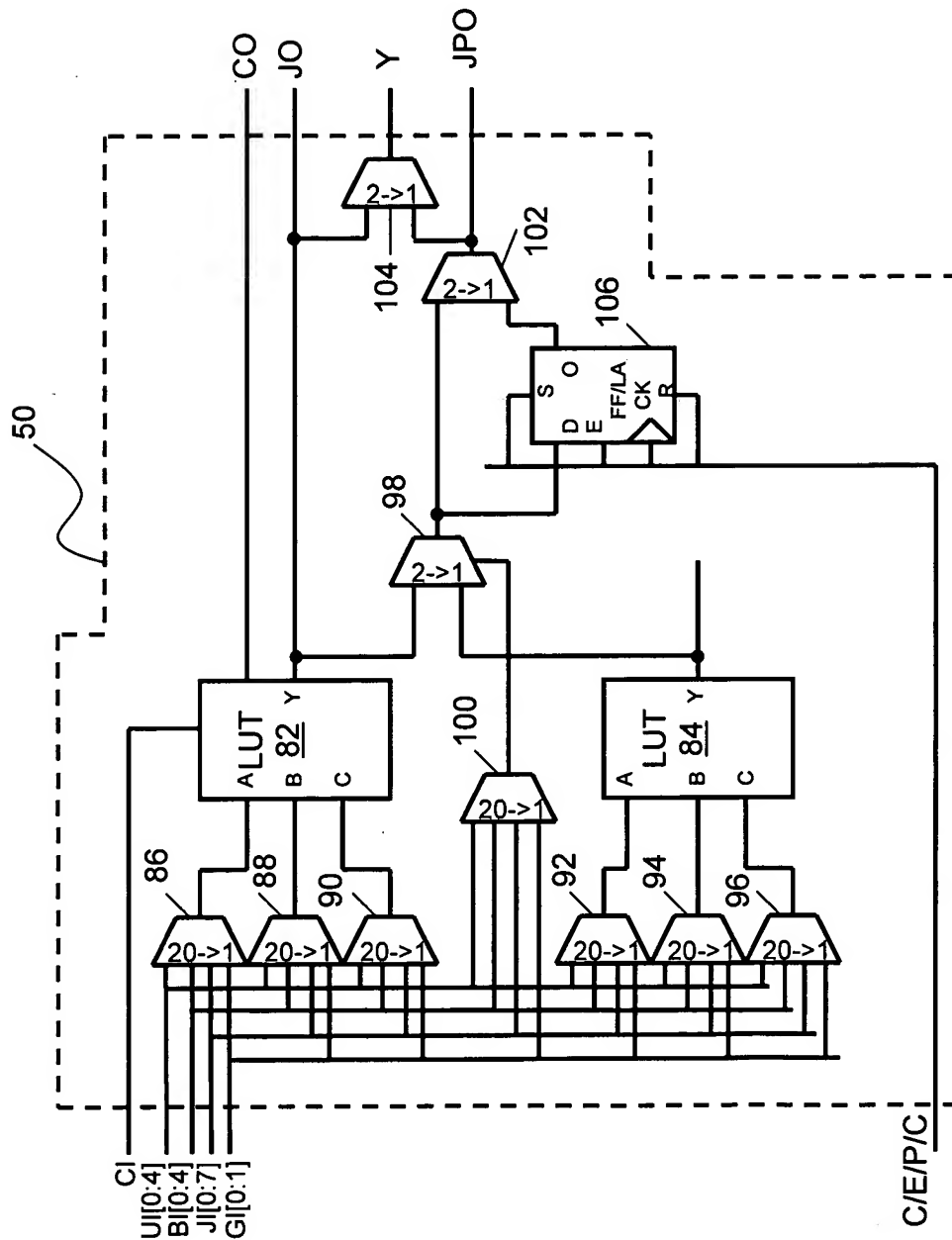


FIG. 10

Replacement Drawing

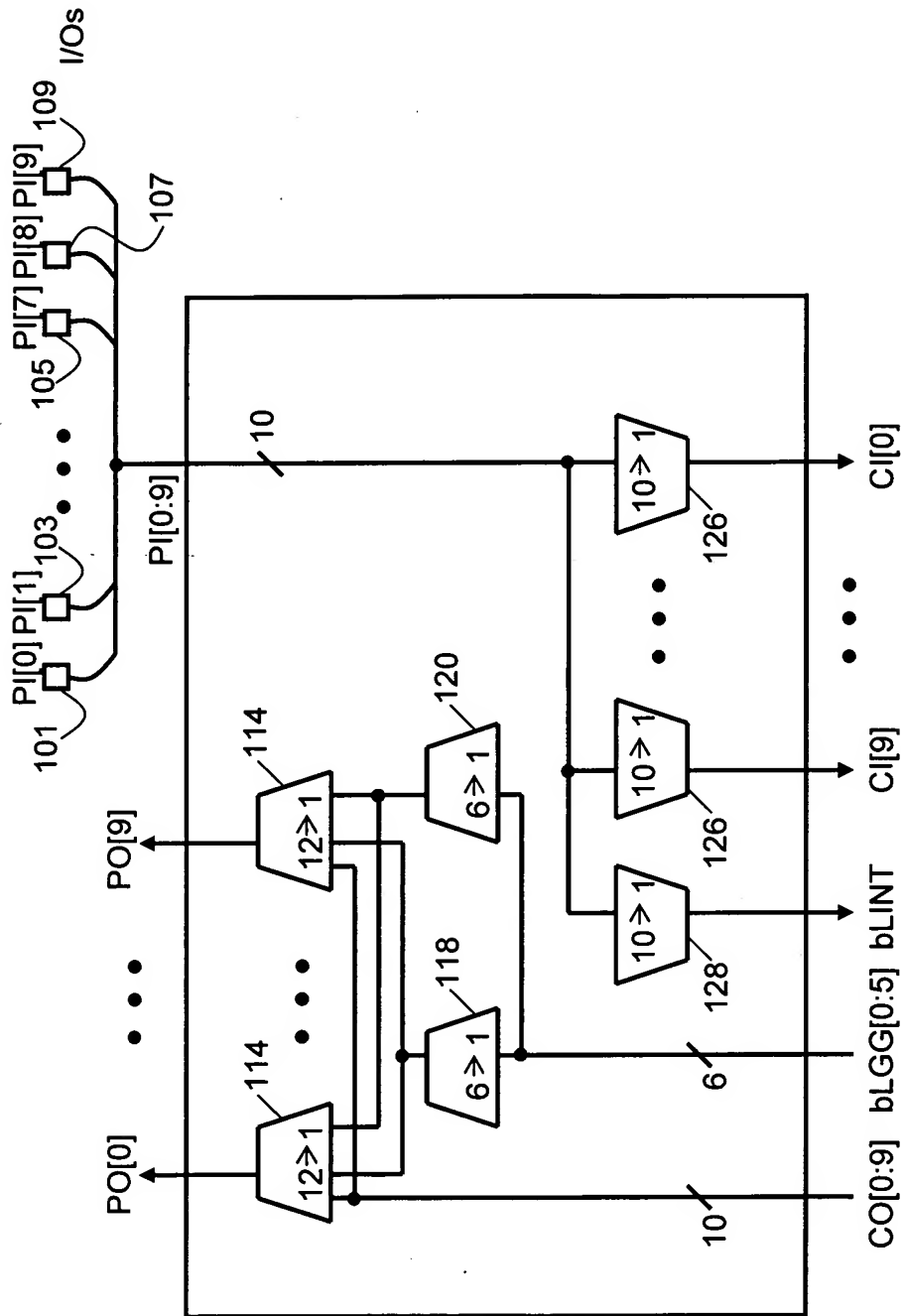


FIG. 12

Replacement Drawing

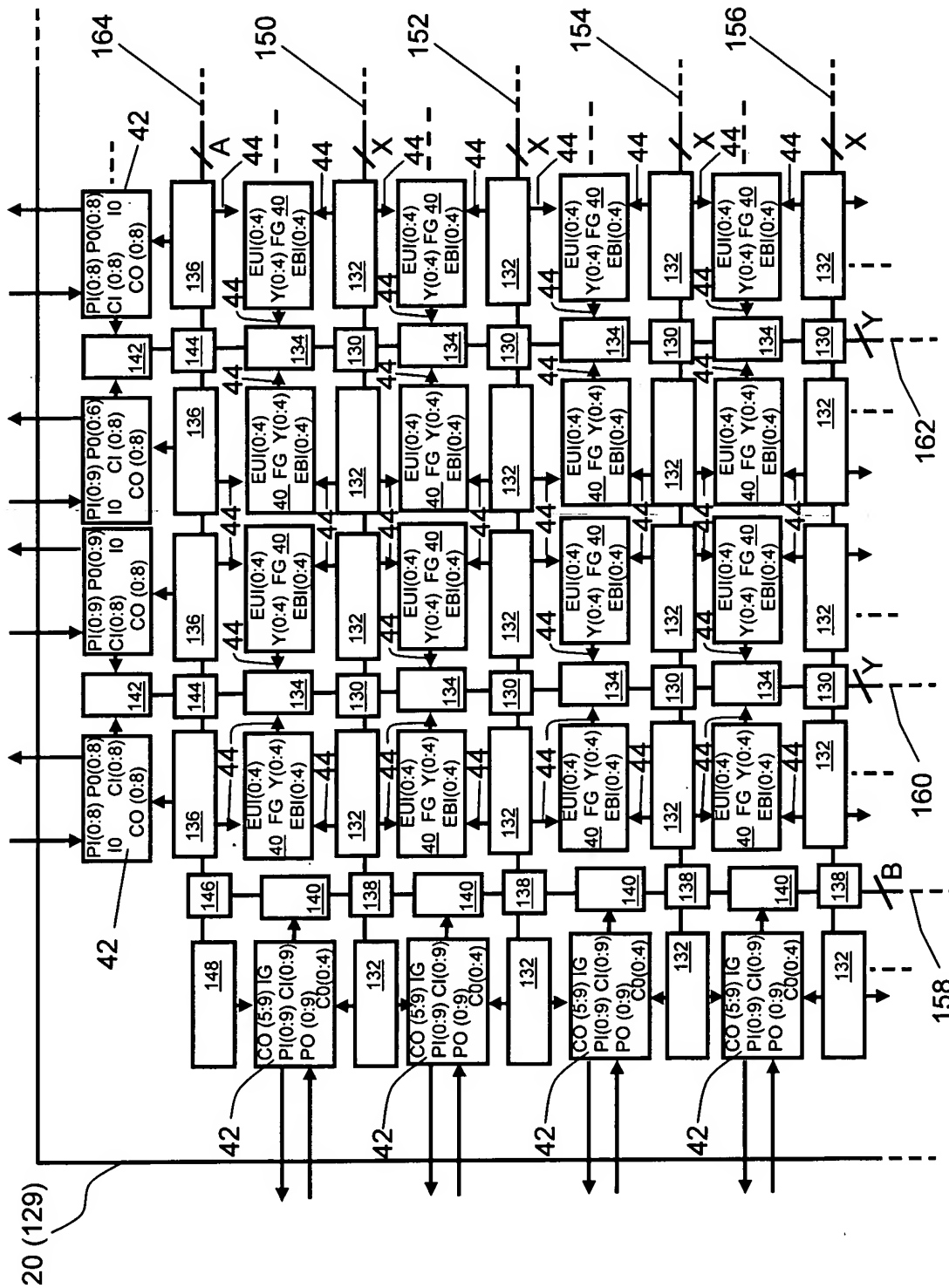


FIG. 13

Replacement Drawing

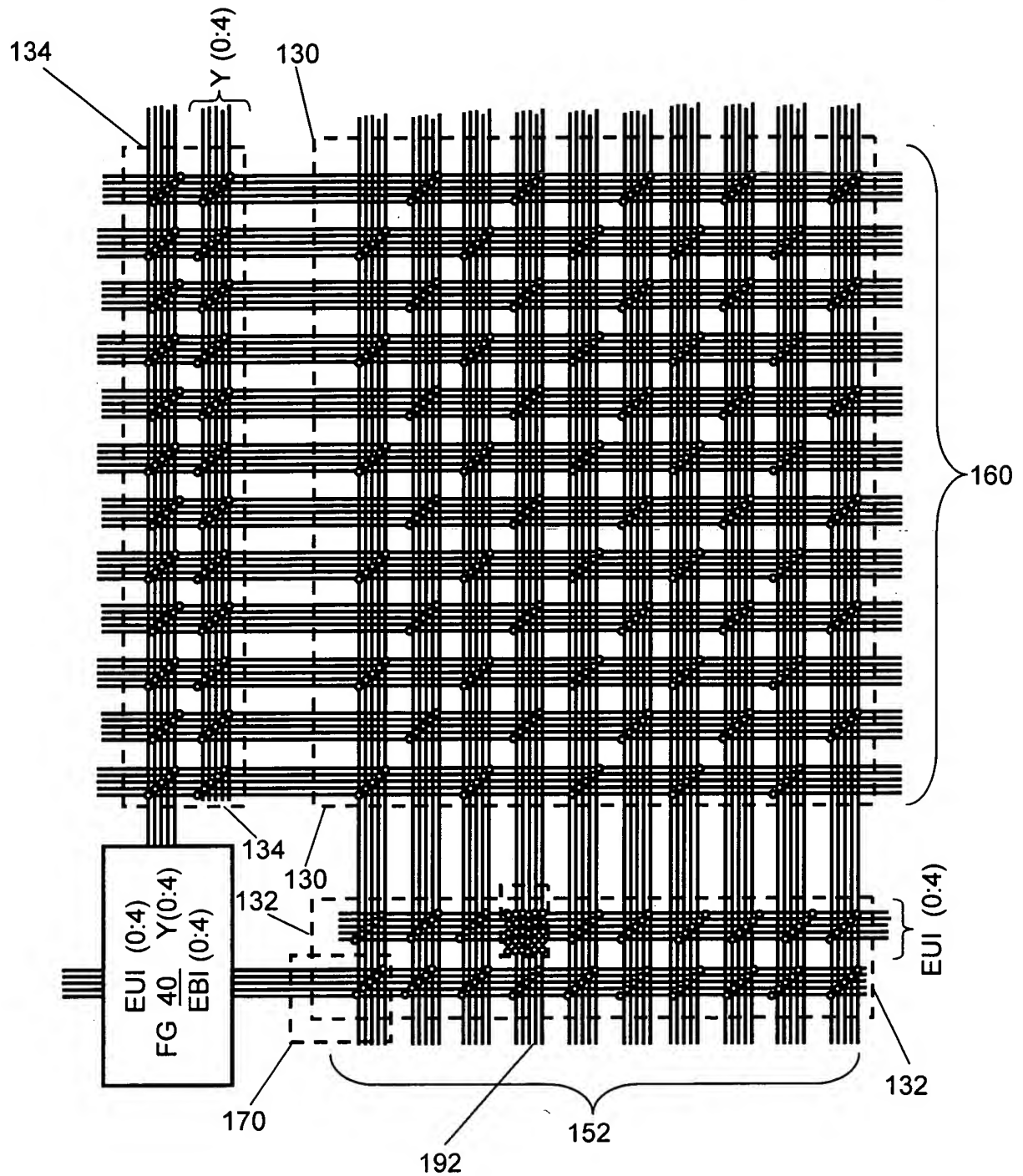


FIG. 14

Replacement Drawing

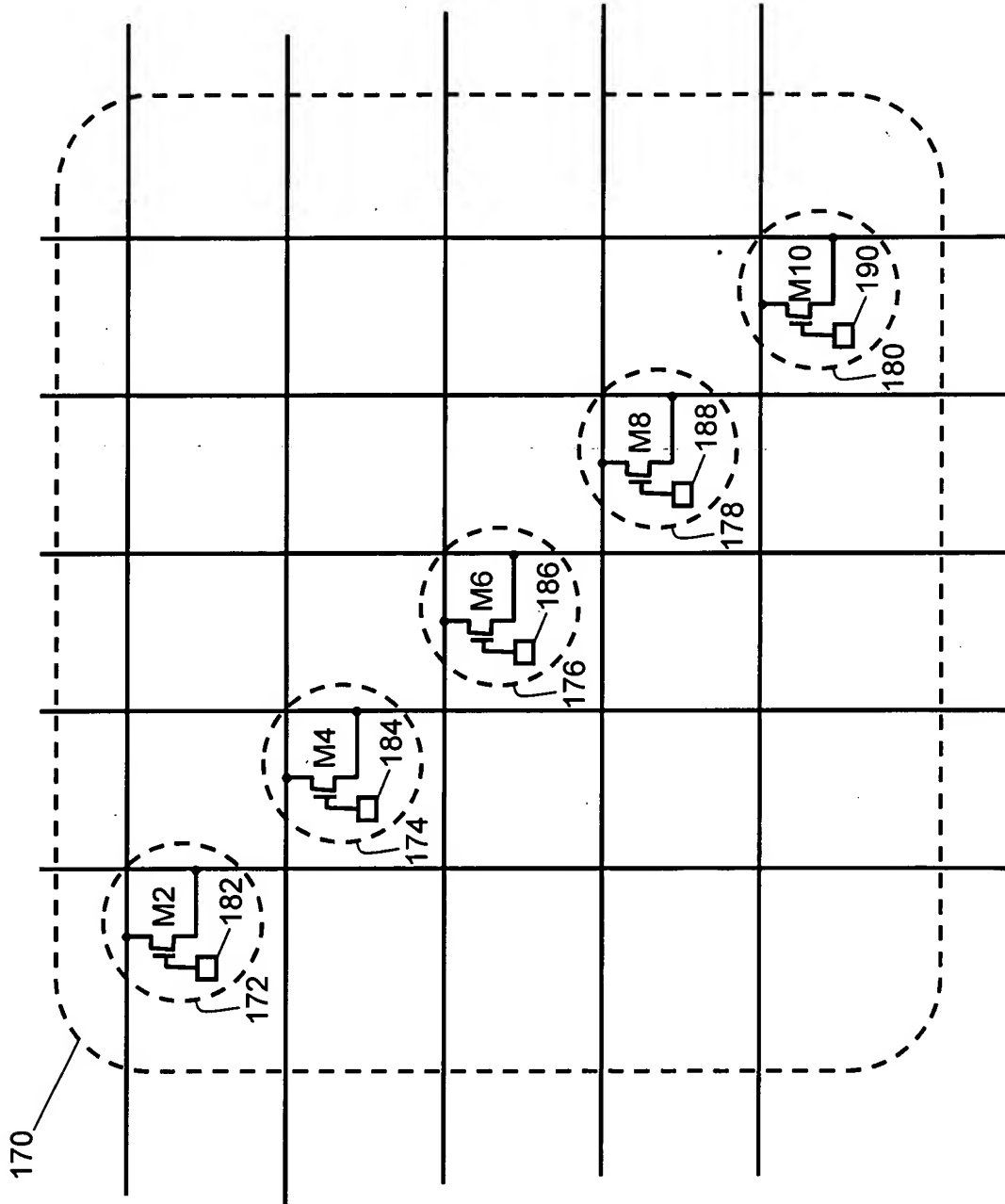


FIG. 15

Replacement Drawing

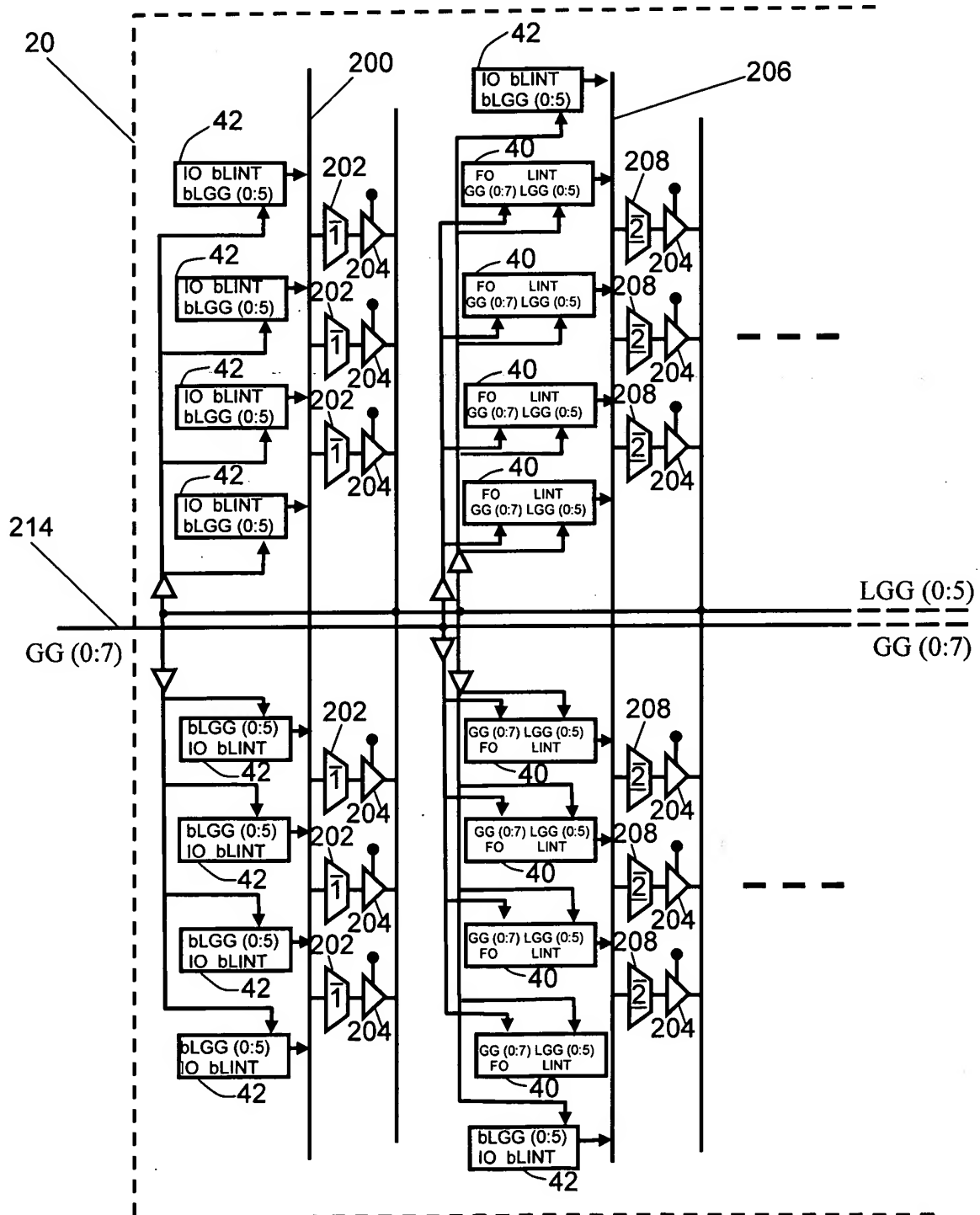


FIG. 16A

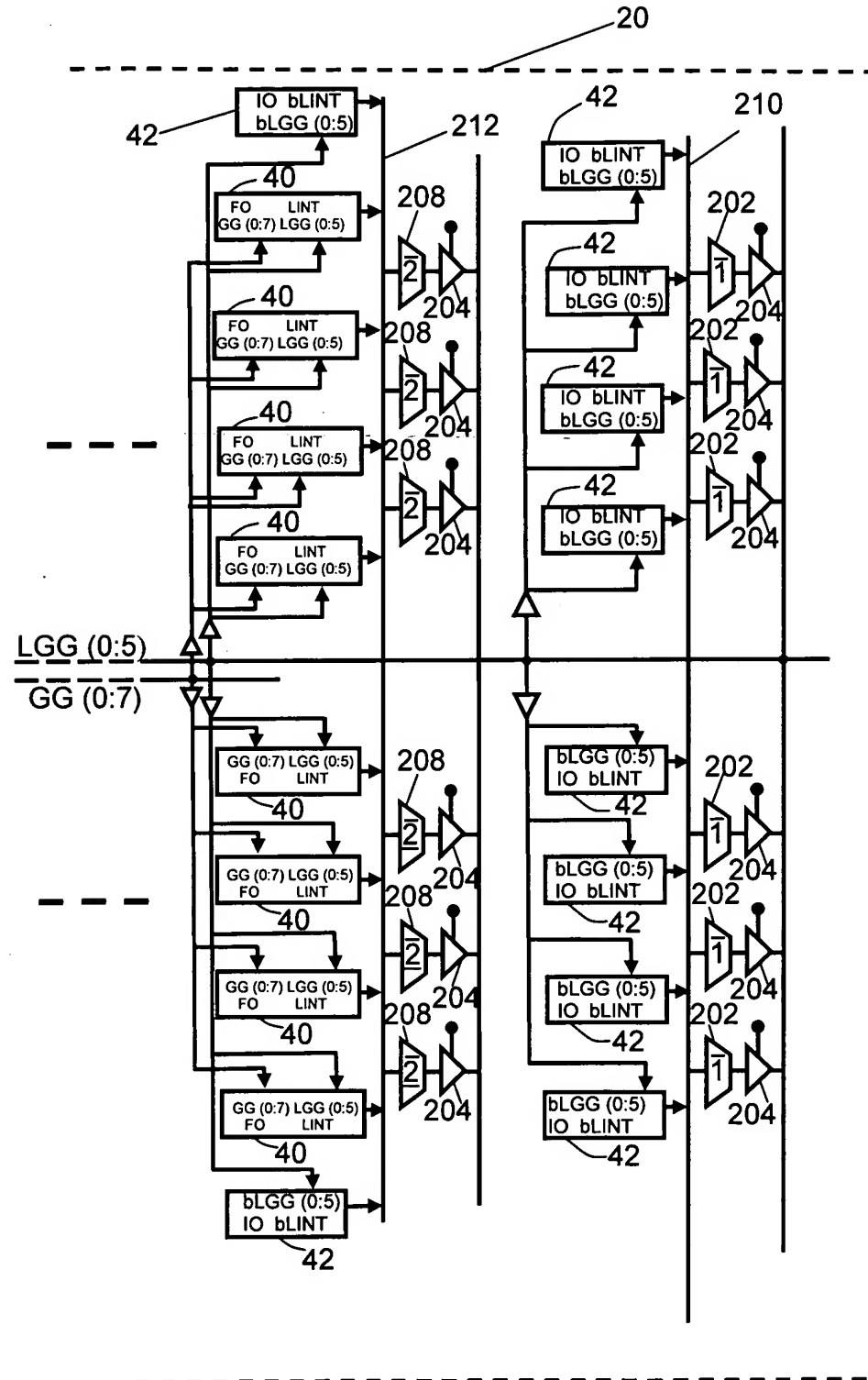


Fig. 16B

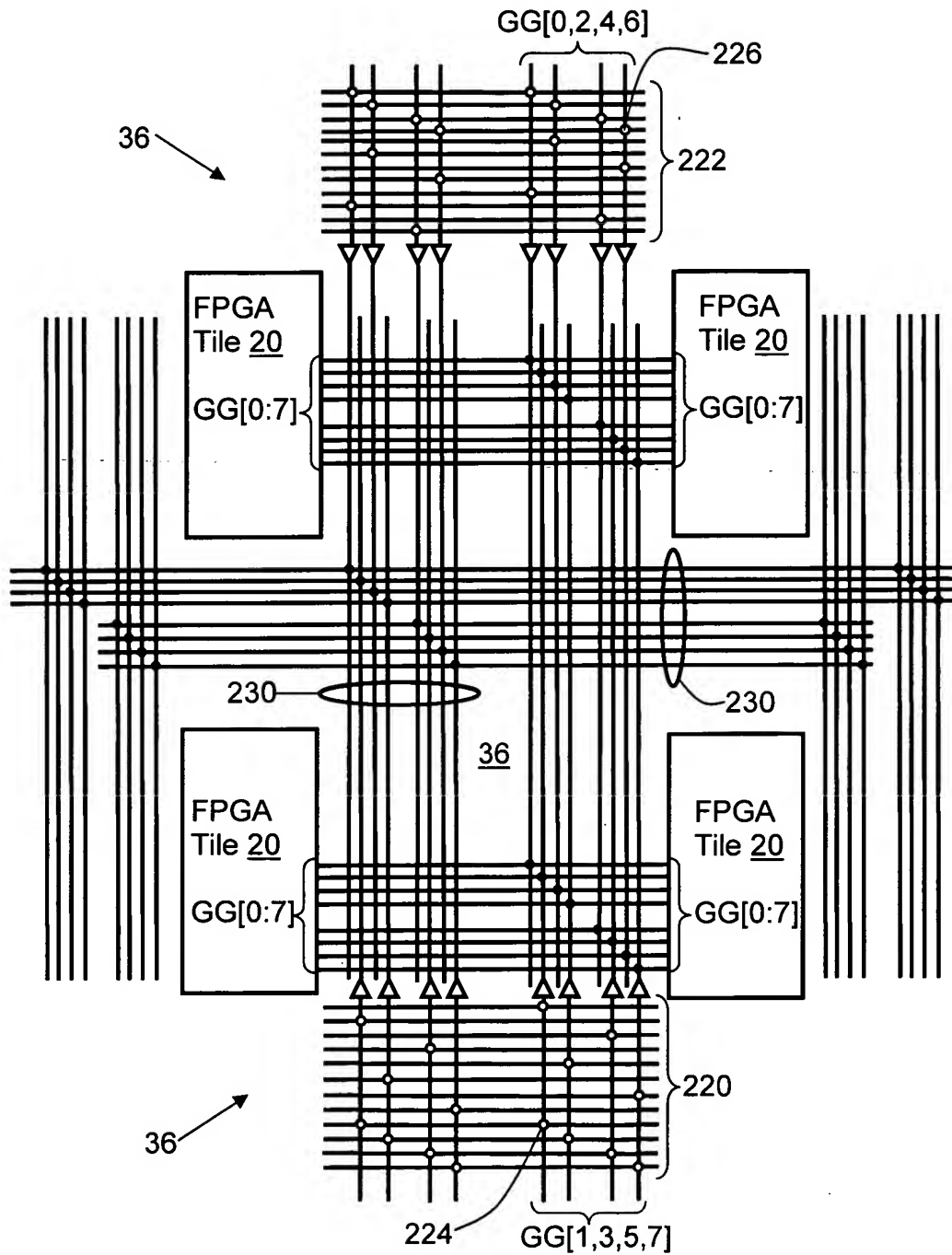


FIG. 17

Replacement Drawing

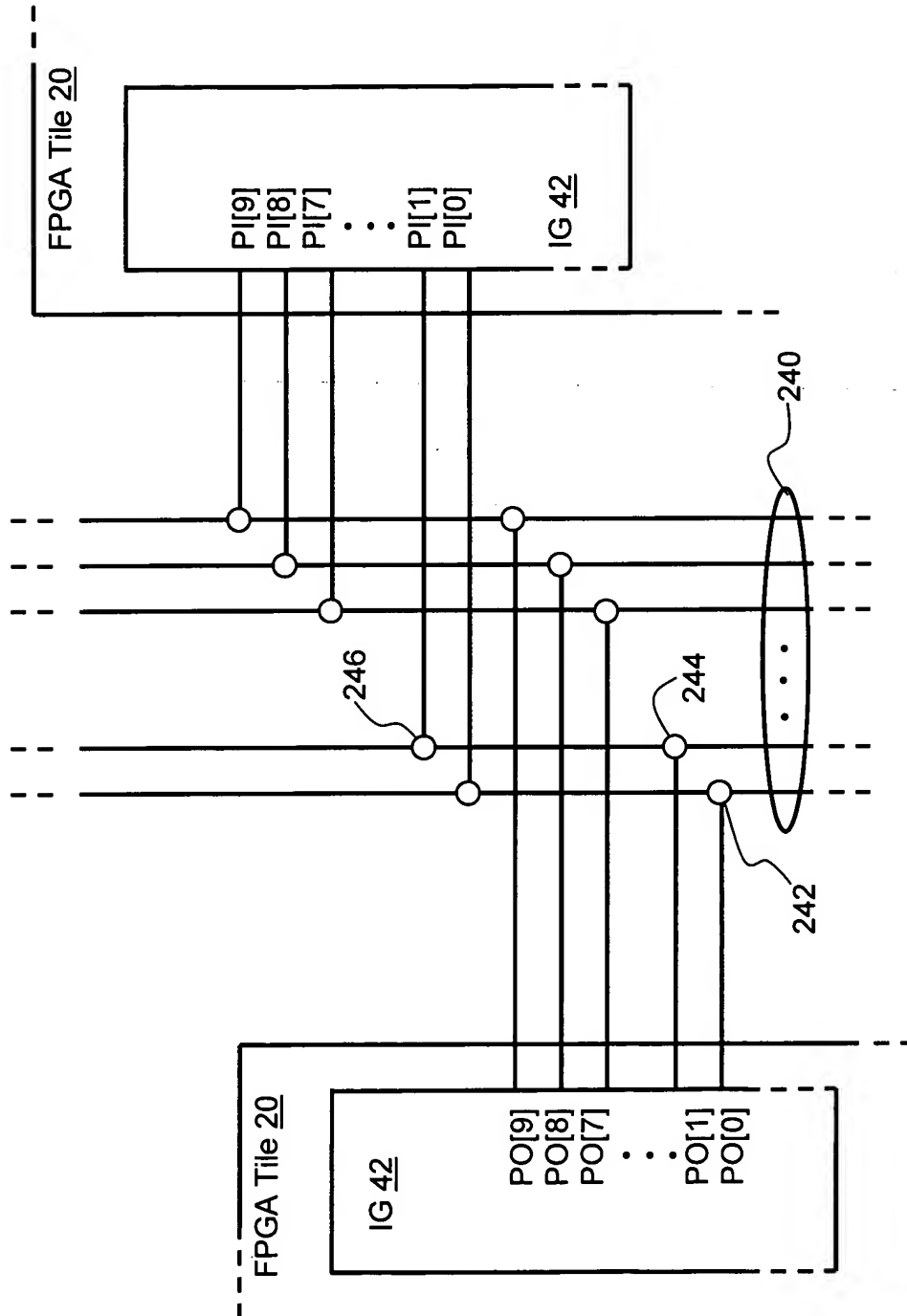


FIG. 18

Replacement Drawing

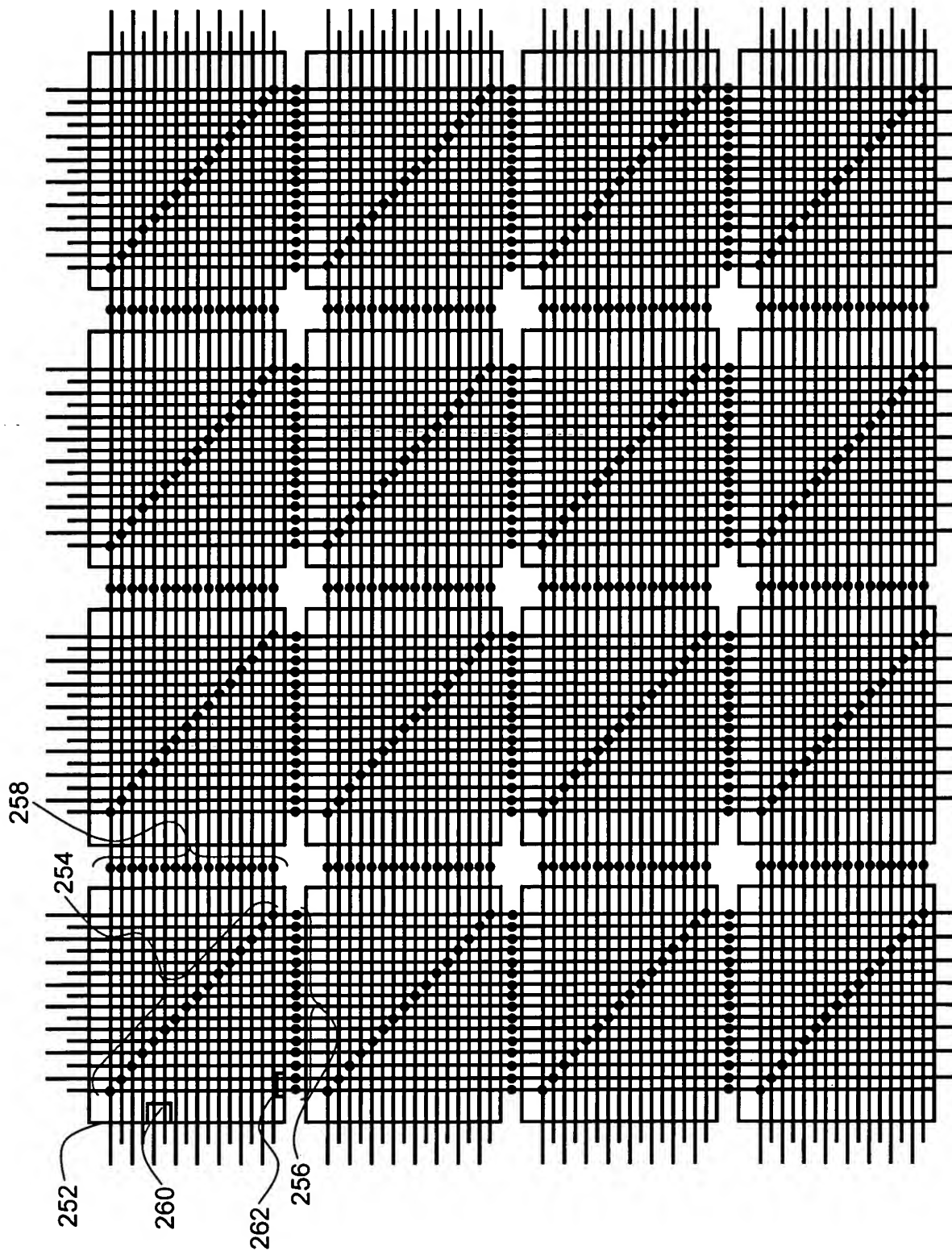


FIG. 19

Replacement Drawing

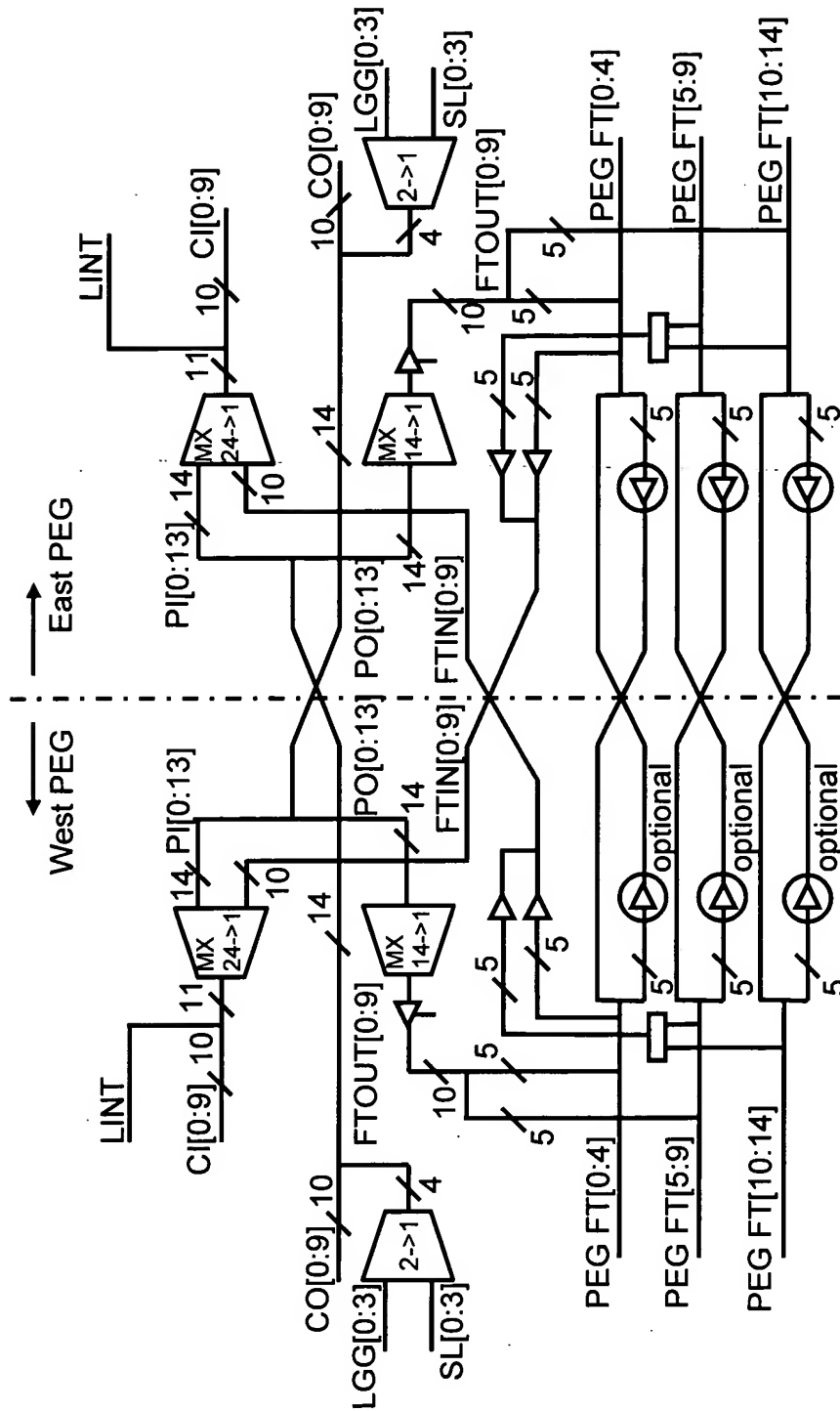


FIG. 20

Replacement Drawing

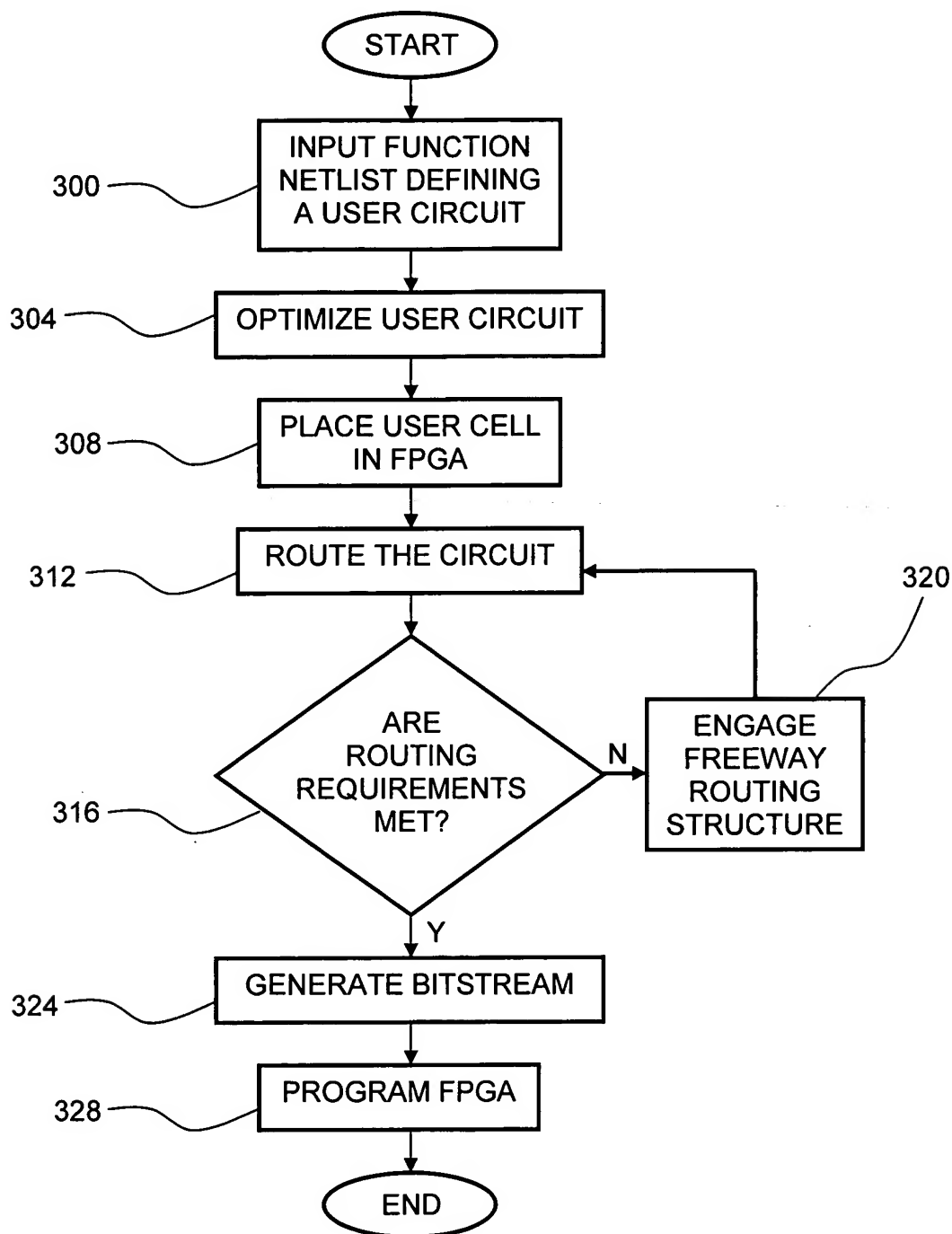


FIG. 21